
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Nguyen et al.

Attorney Docket No.: NSC1P205D2

Application No.: Not yet assigned

Examiner: Not yet assigned

Filed: August 29, 2003

Group: Not yet assigned

Title: TECHNIQUES FOR JOINING AN OPTO-ELECTRONIC MODULE TO A SEMICONDUCTOR PACKAGE

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on August 29, 2003 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number EV334021507US, addressed to the Commissioner for Patents, Washington, DC 20231.

Jeffrey Ng

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:


The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a divisional of prior application U.S. Patent Application No. 09/947,210. This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure

Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. NSC1P205D2).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP


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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. NSC1P205D2	Application No.: Not yet assigned
	Applicant: Nguyen et al.	
	Filing Date August 29, 2003	Group Not yet assigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	5,011,246	04/30/91	Corradetti <i>et al.</i>			
	B	6,305,848	10/23/01	Gregory			
	C	6,201,704	03/13/01	Poplawski <i>et al.</i>			
	D	6,318,909	11/20/01	Giboney <i>et al.</i>			
	E	5,487,124	01/23/96	Bowen <i>et al.</i>			
	F	5,949,135	09/07/99	Washida <i>et al.</i>			
	G	6,043,430	03/28/00	Chun			
	H	6,236,109	05/22/01	Hsuan <i>et al.</i>			
	I	5,139,969	08/18/92	Mori			
	J	6,258,630	07/10/01	Kawahara			
	K	6,316,837	11/13/01	Song			
	L	2001/0013645	08/16/01	King <i>et al.</i>			
	M	6,316,838	11/13/01	Ozawa <i>et al.</i>			
	N	5,726,079	03/10/98	Johnson			

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	O	JP 08-125066	05/17/96	Japan				
	P	60-202956	10/14/85	Japan				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	Q	S. Savastiouk, PH.D., et al. "3-D stacked wafer-level packaging", March 2000, <i>Advanced Packaging</i> , pgs. 28-34.
	R	National Semiconductor, "Packaging Databook", 1993 <i>National Semiconductor</i> , pgs. v-xi to 1-3 to 1-4, 3-1 to 3-20, 3-30 to 3-31, 3-62 to 3-69. Please note: The year of publication is sufficiently earlier than the effective U.S. filing date so that the particular month of publication is not in issue.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	S	5,352,926	10/04/94	Andrews			
	T	5,579,208	11/26/96	Honda <i>et al.</i>			
	U	5,723,369	03/03/98	Barber			
	V	5,019,673	05/28/91	Juskey <i>et al.</i>			
	W	5,608,262	03/04/97	Degani <i>et al.</i>			
	X	5,790,384	08/04/98	Ahmad <i>et al.</i>			
	Y	5,798,567	08/25/98	Kelly <i>et al.</i>			
	Z	2002/0100974	08-2002	Uchiyama			
	AA	2001/0019173	09-2001	Taguchi <i>et al.</i>			
	AB	2001/0048151	12-2001	Chun			
	AC	2002/0047216	04-2002	Jiang <i>et al.</i>			
	AD	2002/0089067	07-2002	Crane <i>et al.</i>			

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							Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
Examiner		Date Considered

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